

## United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/647,224	08/26/2003	Hiroshi Seki	116652	1904	
25944 7	590 04/24/2006		EXAMINER		
	RRIDGE, PLC		CHANG, JOSEPH		
P.O. BOX 1992 ALEXANDRIA	28 A, VA 22320		ART UNIT	PAPER NUMBER	
	, , , , , , , , , , , , , , , , , , , ,		2817	<del></del>	
			DATE MAILED: 04/24/2006		

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)	
	10/647,224	SEKI, HIROSHI	
Office Action Summary	Examiner	Art Unit	
<u> </u>	Joseph Chang	2817	
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address	•
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period w  - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 6(a). In no event, however, may a reply be time ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	N. hely filed the mailing date of this communicat D (35 U.S.C. § 133).	
Status			
3) Since this application is in condition for allowan closed in accordance with the practice under E.	action is non-final.  ce except for formal matters, pro		is
Disposition of Claims			
<ul> <li>4)  Claim(s) 1-7 is/are pending in the application.</li> <li>4a) Of the above claim(s) is/are withdraw</li> <li>5)  Claim(s) is/are allowed.</li> <li>6)  Claim(s) 1-7 is/are rejected.</li> <li>7)  Claim(s) is/are objected to.</li> <li>8)  Claim(s) are subject to restriction and/or</li> </ul>			•
Application Papers			
9) The specification is objected to by the Examiner 10) The drawing(s) filed on is/are: a) access applicant may not request that any objection to the confidence of the second sec	epted or b) objected to by the formal drawing (s) be held in abeyance. See on is required if the drawing (s) is obj	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau * See the attached detailed Office action for a list of	have been received. have been received in Application ity documents have been received (PCT Rule 17.2(a)).	on No ed in this National Stage	
Attachment(s)			
Notice of References Cited (PTO-892)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	•	

Application/Control Number: 10/647,224

Art Unit: 2817

## **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-7 are rejected under 35 U.S.C. 102(b) as being anticipated by JP A 10-190354 (cited by the applicant).

Regarding claims 1 and 6, the JP A 10-190354 reference discloses a semiconductor device (Fig. 1) showing dotted line indicating a crystal oscillator (Xtal) installed outside and an inverting amplifier (INV1, 9, 2, 3) installed inside. Fig. 1 shows a first terminal (T1), a second terminal (T2), a transmission gate (2 and 3), an inverter (INV1), a clamping circuit (9) having a first electrode (drain, ND1), a second electrode (source, 10) and a third electrode (gate) directly connected to a transistor (3) of the transmission gate (2 and 3). It is noted that functional limitation recited in the claim inherently present in the structure because the structure is same as the one in this instant application. It is also noted that a CMOS transistor has an insulated gate.

Regarding claim 2, Fig. 1 shows CMOS transmission gate (2 and 3).

Regarding claim 3, Fig. 1 shows a buffer (INV2 and INV3).

Regarding claims 4 and 7, Fig. 1 shows a transmission gate (4 and 5) that is disposed between the inverting amplifier (INV1, 9, 2, 3) and the buffer (INV2).

Regarding claim 5, Fig. 2 shows a feedback resistor (R).

Art Unit: 2817

## **Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph Chang whose telephone number is 571 272-1759. The examiner can normally be reached on Mon-Fri 0700-1730.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JOSEPH CHANG PRIMARY EXAMINER